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**EMA Partners with Aldec to Provide Cadence OrCAD Users
a Complete FPGA Design Solution**

Rochester, NY (October 14, 2009) – EMA Design Automation™ (www.ema-eda.com), one of the world's largest Electronic Design Automation Value Added Resellers, announced today that it has partnered with Aldec® as an Authorized North American Distributor of Aldec Active-HDL™. This partnership enables EMA to deliver a comprehensive FPGA design environment to Cadence® Design Systems OrCAD® customers. "FPGAs are ubiquitous in electronic design due to their flexibility and time to market advantages," said Manny Marcano, president and CEO of EMA Design Automation. "With these advantages comes the challenge of designing, validating, and implementing increasingly complex logic for these programmable devices. Customers need an FPGA design solution that meets these challenges at a price that fits their budget. With Aldec, EMA is now able to offer its OrCAD customers a complete solution for PCB and FPGA design."

Aldec Active-HDL provides all the capabilities needed for FPGA designers to successfully develop and validate their FPGAs. This includes a full set of HDL design tools, VHDL/Verilog®/SystemVerilog (design) mixed-language simulation, and a flow manager that controls simulation, synthesis, and implementation of Actel®, Altera®, Lattice®, Quicklogic®, Xilinx®, and other FPGAs. Active-HDL supports Windows® Vista/ XP/2003/2000, 32/64 bit operating systems.

Simulation and verification are a large part of the FPGA design process. Proper verification ensures that the design will operate as expected and avoids potential failures after manufacture. With time to market and project schedules shrinking, design size and complexity continue to grow as do the level of difficulty and effort required to verify the design.

Aldec provides users a streamlined and easy-to-use environment to help reduce design and verification time without sacrificing quality.

“Aldec’s 25 years of customer experience, leadership and continual revenue growth in FPGA design and verification are key strengths contributing to our new distribution partnership with EMA. Partnering with EMA gives Aldec a great opportunity to enable PCB and FPGA designers to increase their productivity and extend the reach of our technology,” said Dr. Stanley M. Hyduke, President of Aldec, Inc.

For more information about EMA and its PCB and FPGA design solutions, visit www.ema-ed.com or call 800-813-7494.

About EMA Design Automation, Inc.

EMA Design Automation offers leading product development solutions including electrical CAD tools, mechanical CAD tools, a complete range of product lifecycle management systems, consulting services, training, and technical support. EMA is a Cadence® Channel Partner serving all of North America, an Autodesk® Authorized Value Added reseller, and is an Authorized North American Distributor of Aldec Active-HDL™. EMA manufactures the Component Information Portal™, TimingDesigner®, and CircuitSpace™, and all are distributed through a worldwide network of value added resellers. EMA is a privately held corporation headquartered in Rochester, New York. Visit EMA at www.ema-ed.com for more information.

About Aldec

Aldec Corporation (www.aldec.com) is an industry-leader in Electronic Design Verification and offers a patented technology suite including: RTL Design, RTL Simulators, Hardware-Assisted Verification, Design Rule Checking, IP Cores, DO-254 Functional Verification and Military/Aerospace solutions. Aldec is a privately held corporation and employs approximately 200 people worldwide.

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