



NEW CADENCE VIRTUOSO CUSTOM DESIGN PLATFORM FEATURES TSMC 90-NANOMETER RF PROCESS DESIGN KIT

System Based on New Virtuoso Technology Accelerates Silicon-Accurate Design of Analog, Mixed-Signal, and RF Devices

SAN JOSE, Calif. and HSINCHU, Taiwan, December 12, 2006 -- Cadence Design Systems, Inc. (NASDAQ:CDNS), the leader in global electronic-design innovation, and TSMC (TAIEX: 2330, NYSE:TSM), the world's largest pure-play foundry, today announced the availability of TSMC's 90-nanometer RF Process Design Kit (PDK) for the new Cadence® Virtuoso® custom design platform. The 90-nanometer RF PDK is one in a series of TSMC PDKs supporting the latest Virtuoso platform, a comprehensive system that accelerates silicon-accurate design of analog, mixed-signal, and RF devices.

"Our 90-nanometer RF Process Design Kit for the Virtuoso platform will improve performance and productivity, and accelerate time-to-market for RF designs targeting TSMC's industry-leading technology," said Ed Wan, senior director of Design Services Marketing at TSMC.

"The rapid development of TSMC's 90-nanometer RF PDK for the new Virtuoso platform underscores the importance of Virtuoso technology among innovative mixed-signal and RF designers," said Charlie Giorgetti, corporate vice president of Product Marketing at Cadence. "We look forward to working with TSMC to develop further PDKs with the new Virtuoso platform, in support of our mutual customers' growing demand."

The Virtuoso platform and TSMC's 90-nanometer RF PDK support designers in high-growth IC markets, such as wireless communications. These technologies offer advanced processes for unprecedented integration of digital and mixed-signal designs.

Availability

TSMC's 90-nanometer RF PDK for the latest Virtuoso platform is available through the company's customer web site, TSMC-Online, or by contacting any TSMC account manager.

About TSMC

TSMC is the world's largest dedicated semiconductor foundry, providing the industry's leading process technology and the foundry industry's largest portfolio of process-proven libraries, IP, design tools and reference flows. The Company's total installed capacity in 2006 will exceed seven million (8-inch equivalent) wafers, including capacity from two advanced 12-inch GigaFabs, four eight-inch fabs, and one six-inch fab. TSMC also has substantial capacity commitments at its wholly owned subsidiaries, WaferTech and TSMC (Shanghai), and its joint venture fab, SSMC. TSMC is the first foundry to provide 65nm production capabilities. Its corporate headquarters are in Hsinchu, Taiwan. For more information about TSMC please see <http://www.tsmc.com>.

About Cadence

Cadence enables global electronic-design innovation and plays an essential role in the creation of today's integrated circuits and electronics. Customers use Cadence software and hardware, methodologies, and services to design and verify advanced semiconductors, consumer electronics, networking and telecommunications equipment, and computer systems. Cadence reported 2005 revenues of approximately \$1.3 billion, and has approximately 5,200 employees. The company is headquartered in San Jose, Calif., with sales offices, design centers, and research facilities around the world to serve the global electronics industry. More information about the company, its products, and services is available at www.cadence.com.

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