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EMA CircuitSpace 4.0 Allows Flexible Replication and Design Reuse

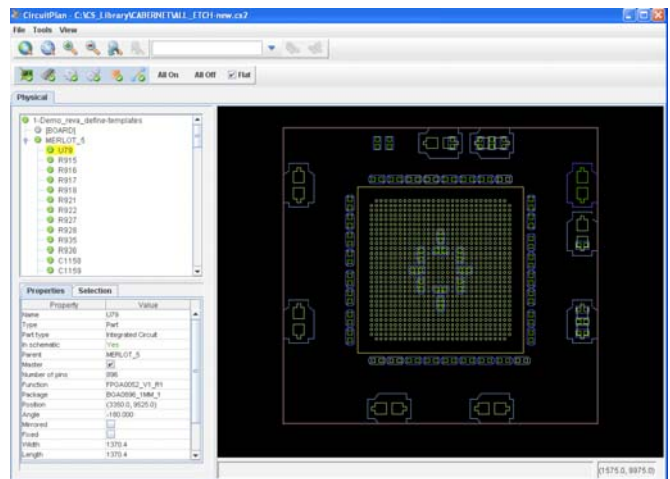
Rochester, NY (March 24, 2010) – EMA Design Automation™ (www.ema-eda.com), a full-service provider of Electronic Design Automation (EDA) solutions, today announced CircuitSpace® version 4.0, which offers advanced placement and design reuse capabilities along with other features to help expedite the PCB layout placement process. “Our development team is dedicated to providing customers with high value added features to improve their PCB design productivity,” said Manny Marcano, president and CEO of EMA Design Automation. “With the new and enhanced features of CircuitSpace 4.0, designers and engineers will dramatically improve their cycle times and increase overall design quality.”

The flexible replication and reuse capabilities, provided by CircuitSpace patented algorithms, enable customers to fully leverage their design IP, even if there are differences between designs.

Customers are no longer burdened with the need to design to the rigid reuse requirements of their tools.

New to CircuitSpace 4.0 are template viewing and cluster mirroring capabilities. The template viewer is a complete graphical user interface (GUI)

enabling the designer to see the parametric information for each component along with an image of the template. This allows designers to review their reuse template



graphically before they apply it to the design in order to ensure they are applying the correct IP for the circuit. The ability to mirror clusters allows templates to be applied where the master component is mirrored, to apply a template etch to a cluster that has been mirrored, and provides the ability to propagate etch from/to clusters that are and are not mirrored. Other enhancements in CircuitSpace 4.0 include a swap cluster command, support for clustering based on a PDF version of the schematic, enhancements to the compare checkpoint report to support testprobe information, and more.

While CircuitSpace is focused on enabling a faster PCB design process, it also contains capabilities that aide the entire design chain. CircuitSpace includes powerful crossprobing tools to allow for joint review of the board and a PDF of the logical schematic. This functionality is of great value to those in the organization that must review, inspect, repair, and ultimately manufacture the board without necessarily having access to the design tools themselves.

“Our designs continue to become more complex; not just in terms of how constrained they are, but also in terms of how dense the parts are,” said Bob Brady, director of engineering, RadiSys® Corporation. “Placement activities were beginning to take 40% or more of the overall layout cycle time. CircuitSpace helped us reduce that time significantly. Our satisfaction with CircuitSpace can really be summed up in the comments we receive from the engineering team like: ‘Wow, you got 6,000 parts placed this quickly? I can’t believe it. Routing is now de-risked, and we have potential upside on the proto delivery schedule.’”

Pricing for CircuitSpace 4.0 starts at \$2,400 (1 year time based license, 1,200 part version). For more information about EMA and its PCB and FPGA design solutions, visit www.ema-eda.com/CircuitSpace or call 800-813-7494.

About EMA Design Automation, Inc.

EMA Design Automation is a leader in product development solutions offering a complete range of electrical and mechanical CAD tools, product lifecycle

management systems, services, training, and technical support. EMA is a Cadence® Channel Partner serving all of North America, an Autodesk® Authorized Value Added Reseller, and is an Authorized North American Distributor of Aldec® Active-HDL™. EMA manufactures the Component Information Portal™ (CIP), TimingDesigner®, and CircuitSpace, and all are distributed through a worldwide network of value added resellers. EMA is a privately held corporation headquartered in Rochester, New York. Visit EMA at www.ema-eda.com or www.ema-eda.com/circuitspace for more information.

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