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PRESS RELEASE

DESIGNADVANCE RELEASES CIRCUITSPACE v2.2 UNVEILS NEW CROSS-PROBING TECHNOLOGY

Pittsburgh, PA (May 12, 2008) – DesignAdvance[®] Systems, Inc., a leading developer of intelligent EDA solutions for PCB design, announced today the release of CircuitSpace[®] v2.2. The new release adds a major new enhancement to CircuitSpace that allows the user to have bi-directional communication between layout and a (PDF) schematic. The new feature gives both Designers and Engineers instant selection and verification of design elements such as components, nets, or pins, within Cadence Allegro and the (PDF) schematic. Through Cross-probing, the user can populate any Cadence Allegro selection set through our proprietary interactive clustering.

Customers have achieved up to 5X returns (2.5 months) on their investment within the first year of using CircuitSpace. Designers reduce board layout and placement time from weeks-to-minutes through CircuitSpace's patented AutoClustering[™], intelligent design (IP) reuse, and Cross-Probing technologies.

"We are impressed with the productivity gains our designers can achieve utilizing CircuitSpace." stated Mike Brown, PCB Development Manager at Ciena Corporation.

Leading technology companies recognize that reductions in PCB design time have a direct impact on time-to-market for new products, which directly correlates to profitability. "Since the initial release of CircuitSpace, we have worked closely with our customers in developing next generation intelligent EDA solutions for PCB design," said Edward Pupa, DesignAdvance CEO. "Now with our new Cross-Probing technology, we see CircuitSpace adding value throughout the entire PCB design cycle" continued Pupa.

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CircuitSpace Featured Capabilities:

- AutoClustering™
- Bi-directional Cross-Probing between Layout and (PDF) Schematic
- Intelligent physical design reuse and replication
- Concurrent layout development project wide
- Template generation for global library usage across divisions
- Template usage with and without etch
- Automated layout reference designator propagation
- Advanced sustaining engineering and ECO process
- Automated change report between layout designs

Availability

CircuitSpace v2.2 is now available.

For product pricing information within North America, please visit EMA Design Automation at www.ema-eda.com

For product pricing information outside of North America, please visit our distribution partners at www.designadvance.com

About DesignAdvance Systems, Inc.

DesignAdvance Systems Inc. develops innovative electronic design software tools that reduce the amount of time it takes to design electrical and mechanical products.

Corporate headquarters are located in Pittsburgh, Pennsylvania. For more information see www.designadvance.com

DesignAdvance is a Cadence Connections Partner.

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