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EMA TimingDesigner 9.25 Automates Static Timing Analysis Process

Rochester, NY (August 31, 2010) – EMA Design Automation™ (www.ema-eda.com), a full-service provider of electrical and mechanical CAD tools, today announced TimingDesigner 9.25 with enhanced Automerge functionality that dramatically decreases the time required for performing interface timing analysis.

“TimingDesigner has always been about accurate, comprehensive timing analysis,” said Manny Marcano, president and CEO of EMA Design Automation. “With compressed design schedules and increased complexity, the need for upfront design analysis and exploration that is both quick and accurate is greater than ever. TimingDesigner 9.25 gives users the tool they need to verify and optimize their design early in the process without impacting their delivery schedule.”

With shrinking timing margins and increased design speeds, comprehensive timing analysis is critical to ensure first pass success. TimingDesigner with the enhanced Automerge functionality allows design teams a reusable, model based approach to their timing analysis. Designers can quickly connect component timing models to create parameterized interface diagrams that can be used to validate timing and interactively test design alternatives. These interface models can be created very early in the design phase to help drive constraint definition and timing budgeting. As the design progresses, it is easy to add data like propagation delay and signal integrity effects to the analysis giving a more accurate representation of timing margins as more information becomes available.

Current maintenance customers also have access to the TimingDesigner Design Kit Library which when combined with the new AutoMerge features provides a very powerful and automated analysis process. Users simply select from the building blocks that make up their analysis, define the connectivity through an easy to use graphical interface, and TimingDesigner generates a parameterized graphical interface timing model for analysis and validation.

Release 9.25 also includes a number of additional enhancements including updates to the Allegro PCB SI flow and new design validation commands to help users identify critical margins, ensure proper analysis, and determine when they have reached timing closure.

For more information about EMA and its static timing, FPGA, and PCB design solutions, visit www.timingdesigner.com/whatsnew or call 800-813-7494.

About EMA Design Automation, Inc.

EMA Design Automation is a leader in product development solutions offering a complete range of electrical and mechanical CAD tools, product lifecycle management systems, services, training, and technical support. EMA is a Cadence® Channel Partner serving all of North America, an Autodesk® Authorized Value Added Reseller, and is an Authorized North American Distributor of Aldec® Active-HDL™. EMA manufactures the Component Information Portal™ (CIP), TimingDesigner®, and CircuitSpace®, and all are distributed through a worldwide network of value added resellers. EMA is a privately held corporation headquartered in Rochester, New York. Visit EMA at www.ema-eda.com for more information.

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